

**Abstract**

Process and device for outputting a digital signal.

- 5 To output a digital signal in particular according to the  
LVDS (low voltage differential signalling) standard, a  
driver stage is supplied with a constant current and thus  
supplies the digital signal in the form of a current signal  
with defined current values. As a result of line  
10 capacitances of a transmission line, because of the current  
limited according to the standard the edge steepness and  
hence the maximum transmittable bit rate can deteriorate.  
According to the invention, therefore, at least essentially  
in synchronization with a triggering of the driver stage,  
15 at least one current increase signal is generated which via  
a capacitor causes an additional current increase in the  
output current of the driver stage. Preferably, the current  
increase signal via the respective capacitor is switched  
directly to an output of the driver stage. By using a  
20 capacitor, with very little expenditure a limited current  
pulse can be switched in a temporally targeted manner on  
the switching processes of the driver stage.

(Fig 2).